

Notice of References Cited	Application/Control No. 10/010,238	Applicant(s)/Patent Under Reexamination BLATT ET AL.	
	Examiner Thomas H. Stevens	Art Unit 2123	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-5,815,693	09-1998	McDermott et al.	713/501
	B	US-6,125,334	09-2000	Hurd, Linda L.	702/60
	C	US-			
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	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)			
	U	Christesen et al., "Analog and Mixed-Signal Modeling Using the VHDL-AMS Language" 36th Design Automation Conference 1999. pg. 1-199			
	V	Tiwari et al., "Power Analysis of Embedded Software: A First Step Towards Software Power Minization" IEEE 1994. p.437-445.			
	W	Greenhill-D. "The Impact of Technology Scaling on Microprocessor Design" Seminar abstract 1999 pg. 1			
	X	Booba et al., "IC Power Distribution Challenges" January 2001. International Conference on Computer-Aided Design IEEE/ACM pg.643-650.			

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.